CLAIMS:

1. An arrangement for processing video signals provided as interlaced video signals generated in the interlaced scanning mode, in which two fields constitute one frame, and/or as pseudo-interlaced video signals derived from non-interlaced video signals obtained by means of progressive scanning, characterized in that at least one video signal-processing unit (1) is provided which receives at least an interlaced video signal or at least a pseudo-interlaced video signal and processes these video signals in dependence upon control data generated by means of a control unit (2), and in that a clock generator (4) is provided which controls the control unit (2) and/or the video signal-processing unit (1) in such a way that, when processing an interlaced video signal or a pseudo-interlaced video signal, possibly new control data are generated and/or taken into account as from the start of its next field or its next frame, respectively.

2. An arrangement as claimed in claim 1, characterized in that a buffer memory (3) is provided for the control data, from which buffer memory the video signal-processing unit (1) directly takes over and employs the control data and which is controlled by the clock generator (4) in such a way that it takes over new control data when processing an interlaced video signal or a pseudo-interlaced video signal at the start of its next field or its next frame, respectively.

3. An arrangement as claimed in claim 1, characterized in that the clock generator (4) controls the control unit (2) in such a way that it supplies new control data to the video signal-processing unit when processing an interlaced video signal or a pseudo-interlaced video signal at the start of its next field or its next frame, respectively.

4. An arrangement as claimed in claim 1, characterized in that the clock generator (4) is controlled by means of a genlock signal which comprises information about the synchronizing signals of the video signal to be processed.

5. An arrangement as claimed in claim 1, characterized in that the video signal-processing unit (1) is provided to mix at least two video signals.

6. An arrangement as claimed in claim 1, characterized in that two video signal-processing units (1) are provided, a first of which is provided to process one or more interlaced video signals and a second is provided to process one or more pseudo-interlaced video signals.

7. An arrangement as claimed in claim 1, characterized in that conversion means (5) are provided to convert non-interlaced video signals into pseudo-interlaced video signals having a scanning line and sync pulse structure which is similar to that of interlaced signals and allows said signals to be processed in the video signal-processing unit.

8. An arrangement as claimed in claim 1, characterized in that reconversion means (6) are provided which convert pseudo-interlaced signals supplied by the video signal-processing unit into non-interlaced video signals and supply them as such.